

RT-M40S85-C00

40Gb/s QSFP+ SR4 Optical Transceiver

Product Features

- Four-channel full-duplex transceiver modules
- Transmission data rate up to 11.3Gbit/s per channel
- Up to 100m on OM3 Multimode Fiber (MMF) and 150m on OM4 MMF
- Low power consumption <1.5W
- Operating case temperature: 0 to 70°C
- 3.3V power supply voltage
- Hot Pluggable QSFP form factor
- MPO connector receptacle
- Built-in digital diagnostic function

Applications

- Proprietary High Speed Interconnections
- Infiniband QDR and DDR interconnects
- Data Center

The Rayoptek RT-M40S85-C00 is a Four-Channel, Pluggable, Parallel, Fiber-Optic QSFP+ Transceiver for InfiniBand QDR/DDR/SDR, 12G/10G/8G/4G/2G fiber channel, PCIe and SAS Applications. The QSFP full-duplex optical module offers 4 independent transmit and receive channels, each capable of 11.3Gbps operation for an aggregate data rate of 45.2Gbps 100m using OM3 fiber. These modules are designed to operate over multimode fiber systems using 850nm VCSEL laser array. An optical fiber ribbon cable with an MPO/MTP connector can be plugged into the QSFP module receptacle. QSFP+ SR4 is one kind of parallel transceiver which provides increased port density and total system cost savings.

Ordering Information

Part Number	Description
RT-M40S85-C00	QSFP+ SR4 100m optical transceiver with full real-time digital diagnostic monitoring and pull tab

For More Information:

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Regulatory Compliance

Feature	Standard	Performance
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022:2010, Class B	Compatible with standards
Electromagnetic susceptibility (EMS)	EN 55024:2010	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class I laser product

Absolute Maximum Ratings

The operation in excess of any absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Notes
Storage Temperature	T _s	-40	85	°C	
Operating Case Temperature	T _{OP}	0	70	°C	
Power Supply Voltage	V _{CC}	-0.3	3.6	V	
Relative Humidity (non-condensation)	RH	0	85	%	
Input Voltage	V _{in}	-0.3	V _{CC} +0.3	V	

Recommended Operating Conditions and Power Supply Requirements

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Operating Case Temperature	T _{OP}	0		70	°C	
Power Supply Voltage	V _{CC}	3.135	3.3	3.465	V	
Power Consumption				1.5	W	
Data Rate	DR		10.3125	11.3	Gbps	
Data Speed Tolerance	ΔDR	-100		+100	ppm	
Link Distance with OM3 fiber	D	0		100	m	

Electrical Characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Test Point	Min	Typical	Max	Unit	Notes
Differential input impedance	Z _{in}	90	100	110	ohm	
Differential Output impedance	Z _{out}	90	100	110	ohm	
Differential input voltage amplitude	ΔV _{in}	300		1100	mVp-p	
Differential output voltage amplitude	ΔV _{out}	500		800	mVp-p	
Bit Error Rate	BR				E-12	
Input Logic Level High	V _{IH}	2.0		V _{CC}	V	
Input Logic Level Low	V _{IL}	0		0.8	V	
Output Logic Level High	V _{OH}	V _{CC} -0.5		V _{CC}	V	
Output Logic Level Low	V _{OL}	0		0.4	V	

Optical Characteristics

All parameters are specified under the recommended operating conditions with PRBS31 data pattern unless otherwise specified.

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Center Wavelength	λ _C	840		860	nm	1
RMS Spectral Width	λ _{rms}			0.65	nm	1
Average Launch Power, each lane	PAVG	-7.6		1	dBm	
Optical Modulation Amplitude (OMA)	POMA	-5.6		0	dBm	1
Difference in Launch Power between any two lanes	P _{tx,diff}			4.0	dB	
Launch Power in OMA minus Transmitter and Dispersion Penalty (TDP), each Lane	OMA-TDP	-6.5			dB	1
Rise/Fall Time	T _r /T _f			50	ps	

Extinction Ratio	ER	3.5			dB	
Transmitter Eye Mask Margin	EMM	10			%	2
Average Launch Power OFF Transmitter, each Lane	Poff			-30	dBm	
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.23, 0.34, 0.43, 0.27, 0.35, 0.4}				
Receiver						
Center Wavelength	λ_C	840	850	860	nm	
Damage Threshold	THd	3.4			dBm	
Overload, each lane	OVL	2.4			dBm	
Receiver Sensitivity in OMA, each Lane	SEN			-9.5	dBm	
Signal Loss Assert Threshold	LOSA	-30			dBm	
Signal Loss Deassert Threshold	LOSD			-12	dBm	
LOS Hysteresis	LOSH	0.5		8	dB	
Optical Return Loss	ORL			-12	dBm	

Notes:

1. Transmitter wavelength, RMS spectral width and power need to meet the OMA minus TDP specs to guarantee link performance.
2. The eye diagram is tested with 1000 waveform.

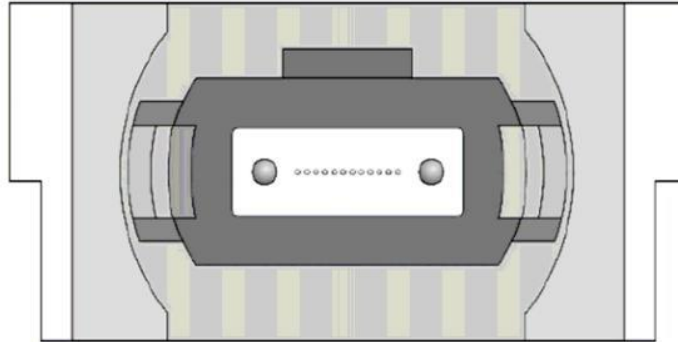
Digital Diagnostic Functions

Digital diagnostics monitoring function is available on all RAYOPTEK QSFP+ SR4. A 2-wire serial interface provides user to contact with module. The structure of the memory is shown in Figure 3. The memory space is arranged into a lower, single page, address space of 128 bytes and multiple upper address space pages. This structure permits timely access to addresses in the lower page, such as Interrupt Flags and Monitors. Less time critical time entries, such as serial ID information and threshold settings, are available with the Page Select function. The interface address used is A0xh and is mainly used for time critical data like interrupt handling in order to enable a one-time-read for all data related to an interrupt situation. After an interrupt, IntL, has been asserted, the host can read out the flag field to determine the affected channel and type of flag.

Parameter	Symbol	Min	Max	Unit	Notes
Temperature monitor absolute error	DMI_Temp	-3	+3	°C	Over operating temperature range
Supply voltage monitor absolute error	DMI_VCC	-0.1	+0.1	V	Over full operating range
Channel RX power monitor absolute error	DMI_RX	-3	+3	dB	1
Channel Bias current monitor	DMI_Ibias	-10%	+10%	mA	

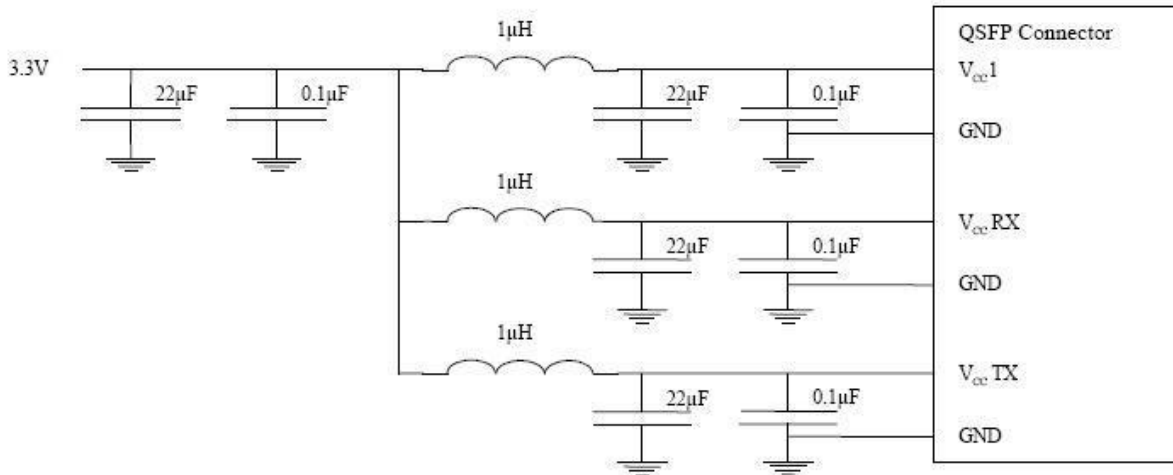
Optical Interface Lanes and Assignment

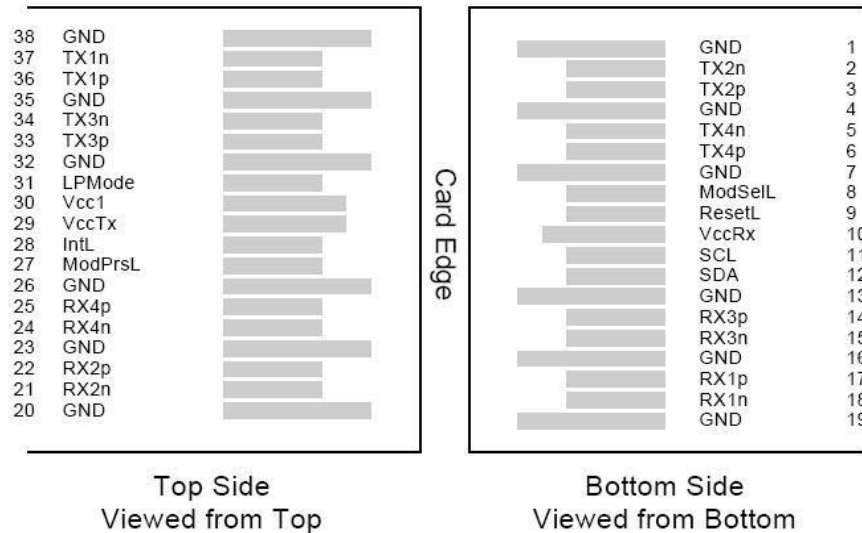
The optical interface port is a male MPO connector .The four fiber positions on the left as shown in Figure 2, with the key up, are used for the optical transmit signals (Channel 1 through4). The fiber positions on the right are used for the optical receive signals (Channel 4 through 1). The central four fibers are physically present.



Transmit Channels: 1 2 3 4
 Unused positions: x x x x
 Receive Channels: 4 3 2 1

Recommended Power Supply Filter





Pin Assignment

PIN #	Logic	Symbol	Description	Notes
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTTLL-I	ModSelL	Module Select	
9	LVTTLL-I	ResetL	Module Reset	
10		VccRx	+3.3V Power Supply Receiver	2
11	LVCNOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVCNOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1

27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1

Notes:

1. Module circuit ground is isolated from module chassis ground within the module. GND is the symbol for signal and supply (power) common for QSFP modules.
2. The connector pins are each rated for a maximum current of 500mA.

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